

And for a software or firmware design, the noise estimator operation may be implemented with codes executed by a processor (e.g., controller 230 or 270 in FIG. 2).

REMARKS

Applicant requests entry of the proposed changes submitted herewith to correct minor deficiencies to the specification and drawings. No new matter has been added. Early consideration and quick passage to issue is earnestly solicited.

Respectfully submitted,

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